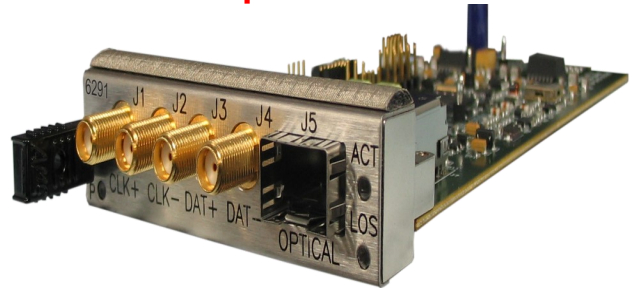


## ECL Data and Clock to fiber Output

### Features

- Two Differential ECL level inputs
  - Synchronous data/clock
  - SMA connectors
- Selectable input impedance
  - 120 ohm
  - High impedance
- Fiber Optic output
  - SFP module
- Pluggable
- Hot Swappable
- 100 kbps to 150 Mbps
- Three LED indicators
- Remote control capable
- Invert Output Polarity

### General Description



The GDP Model 9200-6291 Data Conversion and Distribution Module is one of the functional and hot-swappable modules which can be incorporated into the 9200 Series Chassis. The 9200-6292 Module can be user configured to accept one pair of synchronous ECL level data and clock signals on SMA connectors, combine both data and clock into a single stream, and output this stream over a single fiber optic link via an SFP fiber module. A second module, Model 9200-9162, can be used to accept the fiber optic signal input and re-create the synchronous ECL level data and clock signals on SMA connectors.

Figure 1 shows a functional block diagram of the Model 9200-9162.

The Model 9200 Data Conversion and Distribution System is a modular product that is scalable and user configured to convert and/or distribute a large selection of data channels to satisfy a wide variety of data signal conversion and distribution functions. The 9200 chassis has 16 single height card slots to house the large selection of 9200 series modules. The 9200 series modules provide the ability to accept and properly terminate a variety of signal types and then convert these signals to other industry standards.

The chassis provides global and daisy chain buses so that signals can be received by one module and then sent to other modules in the chassis to satisfy signal conversion and distribution requirements. This concept provides a cost effective and flexible solution to a wide range of signal conversion and distribution applications such as level conversion, signal inversion, code conversion, time code distribution, fiber optic transport, and optical isolation.

## ECL Data and Clock to fiber Output

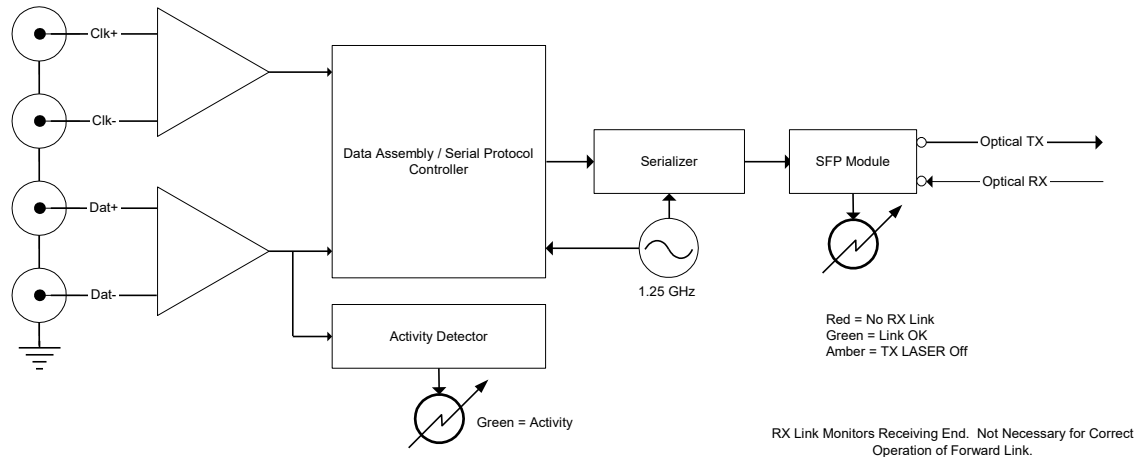


Figure 1: 9200-6291 Module Functional Drawing

## Specifications

### ELECTRICAL

- Two Differential (+/-) ECL level inputs
  - Four (4) SMA connectors
  - Selectable input impedance: 120 ohm or high input impedance.
- One fiber optic output
  - SFP module
- Operates 100 kbps to 150 Mbps
- Hot Swappable module
- Three LED indicators; ACT,LOS,PWR
  - ACT Green: Telemetry present
  - LOS Red: No optical link
  - LOS Green: Optical link is up
  - LOS Amber: Tx (local) laser is off
  - PWR Blue: FPGA is configured

### MECHANICAL

- 2.9" wide
- 0.75" high
- 7.1" deep

### ENVIRONMENTAL

- Operating Temperature
  - 0 degrees C to +55 degrees C
- Operating Relative Humidity
  - 5% to 95% non-condensing
- Non-operating Temperature
  - -40 degrees C to +85 degrees C
- Non-operating Relative Humidity
  - 5% to 95% non-condensing

\* Recognizing that no standard product can meet all the needs of all users, GDP stands ready to provide units tailored to unique applications.

\* The statements in this data sheet are not intended to create any warranty, expressed or implied. Specifications are subject to change without notice.