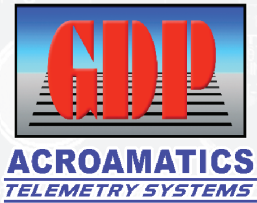


BIT SYNCHRONIZERS

Product Overview



- Bit Rates from 5 bps to 40 Mbps
- Viterbi Decoder & Frame + Subframe Pattern Detector
- Performance within 1 dB of theory
- Locks Maintenance to -3dB Eb/No @ 100 mVp-p
- Auto Bit Rate Scan
- Frame Pattern Detector
- Accepts NRZ-L/M/S, BiØ-L/M/S, DBiØ-M/S, DM-M/S, MDM-M/S, RNRZ-L, RZ bit codes
- Eb/No Signal Quality Measurement
- Synchronization Drop Lock Counter
- QPSK, OQPSK and SOQPSK Resequencer (Optional)
- Built in BERT (Optional)
- Output Data Encapsulation (GDP, DQE/DQM) (Optional)

Model 2265 Single/Dual/Quad PCM Bit Synchronizer is a full function, high-performance bit synchronizer with Viterbi decoding in a 3.5" high rack mount chassis. Available in single, dual, and quad channel configurations supporting data rates up to 40 Mbps, this unit provides superior performance, data quality features, and an optional BERT function for each channel.



Model 2430 Single/Dual PCM Bit Synchronizer is a drop-in replacement for all previous Acroamatics 2430 family 2U rackmount single and dual stream PCM bit synchronizer units. It provides a new signal I/O interface which allows a wide variety of standard unit input and output configurations to be offered at the time of order, making any rear panel connector assignable as to type, signal function and termination.



Model 2365A is available in 4, 6, and 8 channel configurations and housed in a 4U chassis with a full function touch-screen front panel. The bit synchronizers support data rates up to 40 Mbps. These units provide superior performance as well as data quality features and an optional BERT function for each channel.

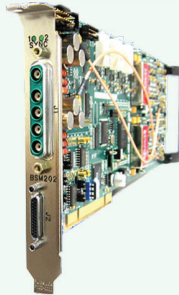


Model 2265EC Multi-Channel PCM Bit Synchronizer/BSS Encapsulator houses up to 4 bit synchronizer channels in a 2U chassis. The optimized digital design of this unit affords the highest performance characteristics currently available. This unit can be used as a stand-alone bit synchronizer and as a data Encapsulator for the GDP Diversity Combiner / Best Source Selector products.



Model BSM202

Dual Bit Synchronizer is a full length PCI Bus card that contains two full function, high-performance bit synchronizers that operate up to 40 Mbps. Complete control of the module is via the PCI Bus. All I/O is provided on card edge connectors.

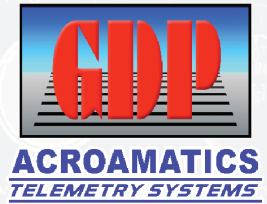


Model 2266C Diversity Combiner/Best Source Selector is a multi-channel bit sync that can use a variety of measurements (i.e. Bit sync Lock, Viterbi Lock, Frame Sync Pattern Lock and measured Eb/No) to determine and select the best source for further processing. Use of Eb/No measurement allows the best source selection of encrypted or unknown streams. Clock phasing and data correlation allow seamless switching from source to source while maintaining phase relationships between sources. Several dB of combining gain is achieved in the reconstructed output stream. See GDP's Best Source Selector Product Overview for additional product offerings.



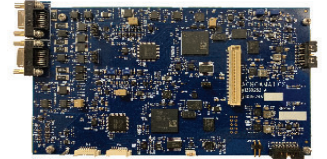
BIT SYNCHRONIZERS

Product Overview

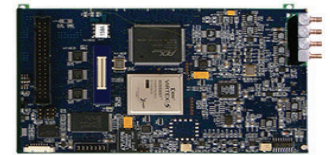


Bit Sync Mezzanine Cards

Model 674DM is a Dual stream range quality PCM Bit Sync companion mezzanine for use with the Model 1632AP. The 674DM is a compact 'mezzanine' design that mounts flush to the Model 1632AP. The 674DM utilizes the latest techniques in FIR filtering, digital phase locked loop, NCO clock reconstruction, and digital amplitude and offset control to deliver high performance, reduced parts count and improved reliability. The 674DM supports a full array of user selectable input/output codes and modes, including both frame and BERT validation loop test options.



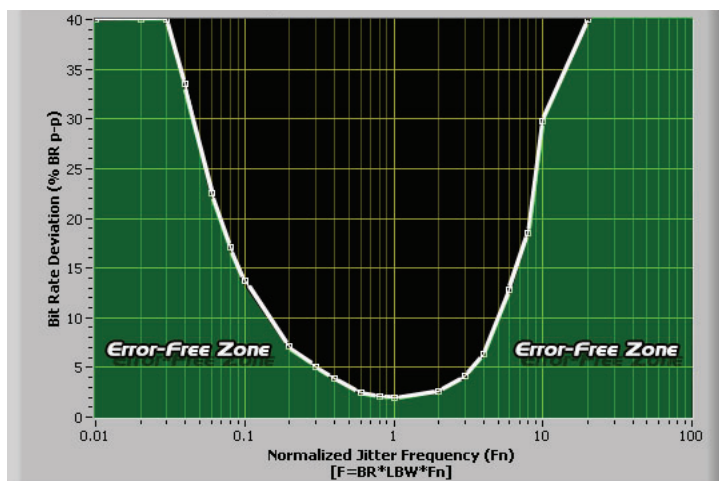
Model 474DM is a Single Stream version of the Model 674DM Dual Bit Sync mezzanine, the 474DM is designed to flush mount to all of Acroamatics single stream multi-function PCM Decom cards.



Model 650 Data Transmission Test Set fills the need for high-performance data-link verification and qualification at an affordable price. The user is provided with totally independent transmit and receive functions to allow rapid fault isolation and data link characterization. The Model 650 Test Set is perfect for Bit Sync testing. It can plot Bit Sync performance in both noise and jitter at the push of a button.



Bit Synchronizer Jitter Performance



Bit Synchronizer Noise Performance

